Technical Data
Schroff CPCI - Backplanes

part# 23006 – 7nn & 8nn

Mechanical and Climatic Parameters	**Standard**	**on request**
Operating Temperature	-40°C - +105°C	-55°C - +155°C
Storage Temperature	-55°C - +105°C	-55°C - +155°C
Humidity conformal coating	max 95%, not condensing	 on request
Flammability: • PCB, Connectors • Ceramic caps	 UL 94 V-0 fire-proof	
Connectors • Performance level per IEC 61076-4-101 • Mechanical Durability (Mating Cycles) • Total Insertion and Extraction Force (mating)	IEC 61076-4-101 (HardMetric 2mm Grid) level 2 > 250 cycles < 0,7 N / Pin	 level 1 > 500 cycles
Vibration acc. DIN 41640 Part 15	10Hz – 500Hz 5g rms	5Hz – 2000Hz 20g rms
Shock (10 pulses each direction x,y,z)	10g, 6ms	
Low Pressure / Altitude (max Board voltage per single isolation gap doesn't exceed 12V)	no restrictions	
Construction:	10 - Layer Stripline	
Dimensions (mm) • Width (pl. see Dwg.) • Height 3U / 6U • Thickness	 20,32mm x # Slots-1mm 128,7mm / 262,05 mm 3,2 mm +/- 0,2 mm	

Electrical Parameters:

Specifications	PICMG 2.0 R3.0 PICMG 2.1 PICMG 2.6 PICMG 2.9 PICMG 2.10	CPCI Core Specification CPCI Hot Swap Specification Bridging Specification System Management Bus Spec. Keying Specification
Service Life: MTBF, acc. to MIL HDBK 217F, cond.: 25°C, ground, benign 6U 8-Slot	more than 600.000h	
Characteristic Impedance PCI traces	65 Ω ± 10 %	
Ohmic Resistance of Signal Tracks PCI traces	< 95mΩ/Slot	
Hot Swap	supported	

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Electrical Parameters (II):

Termination (only 8 Slot Backplanes)	Schottky diodes (on request), plugable termination board
Power input	• Power bugs for wiring or • special Adapter Board to use an ATX cable; this board can act as a power distribution star point within the Systems
max. Current Carrying Capacity of power planes • 5V/GND • 3,3V/GND	 10 A per Slot 10 A per Slot
max. Voltage Drop between any two point on teh backplane on +5V or +3,3V	< 40mV
VI/O • bridging (default) • on request	+5V (default), blue key; 3,3V optional (yellow key) • field changeable, using M4 screws and a bus bar • fixed during bp assy by using a Power Bug cable using Faston crimp contacts
Clock frequency	33 MHz 66 MHz up to 5 Slots; on higher Slot number M66EN can by enabled for test purposes (cut a copper link on rear)
PCI Bus Width	32bit; 64bit, check part#
Data Transfer Rate (peak) • 33 MHz • 66 MHz	 132 Mbyte/s (32 bit) / 264 Mbyte/s (64 bit) 264 Mbyte/s (32 bit) / 528 Mbyte/s (64 bit)
Bridging of Backplanes clock frequency: primary / secondary • 33 MHz / 33MHz • 66 MHz / 33MHz • 66 MHz / 66MHz	backplane of slot numbers equal or higher than 4 up to 7 Slots can be bridged primary / secondary • any slot number as primary and secondary b/p • 4 Slots / any Slot number • 4 Slot / 4 Slot